

**AMENDMENTS TO THE SPECIFICATION**

Please amend the paragraph beginning at page 20, line 9, as follows:

FIG. 6C is a flow chart of a process performed when there has occurred an OFF event of the AUX bus selecting switch 203. If the register AS is not currently set at "2" as determined at step 631, it means that the OFF event has occurred before the A timer reaches its predetermined count, so that the A timer is deactivated at step 632 and the AS register is initialized to "0" at step 634. After that, the process is brought to an end. If the register AS is currently set at "2", it means that the current operation mode is the mix-minus mode, so that the CPU proceeds to step 633, where the current ON/OFF ~~settings states~~ of the signal input (delivery) from the individual input channels of the currently-selected layer to the AUX bus of the bus number AN are displayed by ~~illumination/deillumination~~ of the LEDs of the ch\_ON switches 212 corresponding to the input channels. Then, the CPU proceeds to step 634.